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Adobe Captivate 6 Serial Crack ios (PDF, ebook, Word) Adobe Captivate 6 Crack has many features that are unique to it. It has many features that make it stand out from other. The Adobe Captivate Client Book 6 enables students to create captivating, interactive. The present invention relates to a semiconductor device, and more particularly to a process for manufacturing a MOS (Metal-Oxide-Semiconductor) transistor having a low specific on-resistance. In recent years, the use of a MOS transistor has spread to various electronic devices, and the development of its manufacturing technique is going on, but it is not easy to lower the specific on-resistance of MOS transistors. That is, the specific on-resistance of a MOS transistor is a resistance per unit area of the MOS transistor and is determined by the source/drain diffusion resistance (on-resistance) in a MOS transistor, a parasitic resistance in a wiring line, the sheet resistance of a gate electrode, etc. The on-resistance of a MOS transistor is represented by $R_{\text{sub.on}} = V_{\text{sub.d}} \cdot d / q \cdot \text{sup.2} \cdot \mu \cdot n \cdot \text{sub.s} \cdot \text{sup.2}$ (1) where $V_{\text{sub.d}}$ is the drain potential, q is the elementary charge, $\mu \cdot n \cdot \text{sub.s}$ is the specific on-resistance of the MOS transistor, and d is the source-to-drain distance. Since the resistance of a channel is represented by the channel width $W_{\text{sub.CH}}$ and the channel length $L_{\text{sub.CH}}$ as described below, the specific on-resistance can be represented by the following equation (2). $R_{\text{sub.on}} = \text{OMEGA} / q \cdot \text{multidot} \cdot (L_{\text{sub.CH}} / W_{\text{sub.CH}})^2$ (2) where OMEGA is the sheet resistance of a gate electrode, etc. Accordingly, the specific on-resistance is proportional to the value of $W_{\text{sub.CH}}$, $L_{\text{sub.CH}}$, and OMEGA , and is inversely proportional to the value of q . As shown in FIG. 1, a conventional MOS transistor is formed by a step of forming a buried oxide film 101 for forming a P-type well region on the surface of a silicon substrate 100, a step of forming an N-type diffusion 6d1f23a050

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